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PATENT NUMBER and
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U.S. UTILITY Patent Application

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**APPLICANTS: Tamitani Naoki; Kogure Rie; Takaoka Yuji; Park Seayoul; Takakura Yasushi; Yamauchi Hideyuki;					
**CONTINUING DATA VERIFIED: This application is a 371 of PCT/JP00/02914 05/02/2000					
** FOREIGN APPLICATIONS VERIFIED:					
PG-PUB <input type="checkbox"/>		DO NOT PUBLISH <input type="checkbox"/>		RESCIND <input type="checkbox"/>	
Foreign priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no 35 USC 119 conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no Verified and Acknowledged Examiners's initials <i>[Signature]</i>				ATTORNEY DOCKET NO SOE1/0016	
TITLE : Semiconductor device and method of manufacturing semiconductor device					
U.S. DEPT. OF COMM./PAT. & TM.-PTO-436L (Rev. 12-94)					

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Total Claims <input type="checkbox"/> Print Claim for <input type="checkbox"/>	
ISSUE FEE		DRAWING	
Amount Due	Date Paid	Sheets Drawn	Figs. Drawn
		Print Fig. <input type="checkbox"/>	
<input type="checkbox"/> TERMINAL DISCLAIMER		Application Examiner	
		PREPARED FOR ISSUE	
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